48165	MAIN CHASSIS ASSY
48162	CONTROL PANEL ASSY
39859	INPUT POWER ASSY
39858	PC BOARD ASSY, INPUT
47811	OUTPUT STAGE ASSY

89926	SCREW, WING 6-32
38332	SCREW, R.H. 8-32
89582	NUT 8-32
83025	SPEED NUT 8-32
82666	BOTTOM COVER
94284	FOOT
10045	SCREW #6 SM
11638	WASHER, FLAT #6
89793	SCREW #8 X 5/16 HH
12875	LABEL, CAUTION
11477	CLAMP, CABLE 5/16

REF DES	LAST USED
B1	S3
C30	T2
CR16	X6
DS2	P2
F2	
L1	
M1	
Q14	
R47	

NOTES: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS IN OHMS, 1/2 WATT, 5% EXCEPT AS NOTED.
2. CAPACITORS IN MICROFARADS. THOSE OVER 1.0 μFD ARE POLARIZED ELECTROLYTICS, POLARITY SHOWN.
3. J.B.L. RESERVES THE RIGHT TO MAKE MINOR COMPONENT CHANGES WITHOUT NOTICE.
4. APPROVED ENG SAMPLE, MODEL 6015B, CURRENT REVISION, MAINTAINED IN ENG. DEPT.
5. SEE DWG 48217 REF FOR TEST SPECIFICATIONS.
6. XX IS DC VOLTAGE WITH VTVM REFERRED TO CHASSIS GROUND WITH NO SIGNAL.
7. AT 120 W OUT, VU METER TO READ ZERO.
8. ADJUST R_{X1} FOR 2.15 MV MIN, 4 MV MAX ACROSS 0.25Ω COLLECTOR RESISTOR AT THE OUTPUT TRANSISTOR. VOM LEADS MUST BOTH FLOAT FROM CHASSIS GND POTENTIAL.
9. ADJUST R_{X2} FOR 1/2 OF (V) AT POINT B.
10. ALL GROUND SYMBOLS ARE PHYSICALLY CONNECTED TO ONE COMMON CHASSIS POINT.
11. APPLY ZINC OXIDE THERMAL COMPOUND BETWEEN OUTPUT TRANSISTORS (Q7-Q12) AND INSULATOR, AND INSULATOR AND HEAT SINK.
12. DIRECT OUT 4Ω: REMOVE WHT PRIMARY WIRE OF T2 AT POINT A, AND WITH 18 AWG JUMPER CONNECT POINT A TO PIN 9 OF X6.

REVISIONS			
LTR	DESCRIPTION	DATE	BY
A	REL FOR PROD	8/13/71	
B	REL'D	9/21/71	
C	REL'D	10/27/71	

DRAWN	9-24-71
CHECKED	9/24/71
ENGR	
APPROVED	
MFG	

MFD ITEM – 60

SIZE D | MFD ITEM DWG | DRAWING NUMBER 48165

SCALE | WEIGHT | SHEET 1 OF